

02-C-085 19/603,239 BEST AVAILABLE COF

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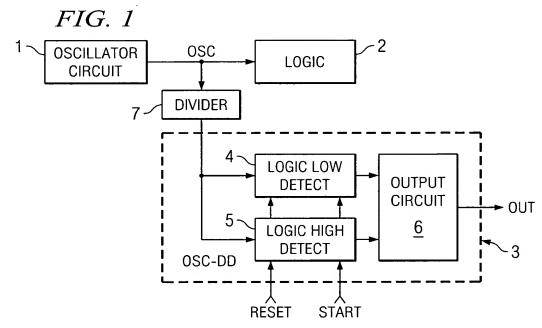
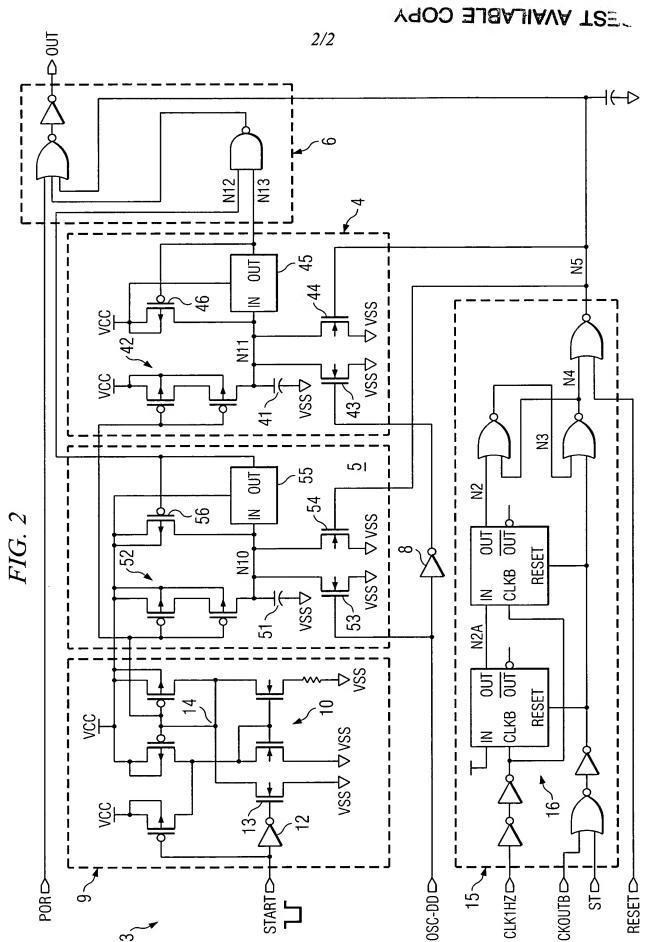


FIG. 3 START CURRENT SOURCES RESET LOGIC LOW DETECT CIRCUIT AND LOGIC HIGH DETECT CIRCUIT APPLY SIGNAL OSC-DD HAVING DIVIDED-DOWN FREQUENCY TO LOGIC LOW AND LOGIC HIGH DETECT CIRCUITS **DOES SIGNAL DOES SIGNAL** OSC-DD REMAIN IN OSC-DD REMAIN IN NO NO LOGIC LOW STATE FOR A LOGIC HIGH STATE FOR A **PREDETERMINED PREDETERMINED** TIME? TIME? YES YES PLACE OUTPUT SIGNAL **OUT IN ACTIVE STATE**



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